



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

**Takehiko MAKITO *et al.***

Application No: 10/717,547

Confirmation No. 7976

Filed: November 21, 2003

For: **SURFACE TREATMENT METHOD FOR  
A COMPOUND SEMICONDUCTOR  
LAYER AND METHOD OF  
FABRICATION OF A SEMICONDUCTOR  
DEVICE**

Art Unit: 1765

Examiner: **Duy Vu Nguyen Deo**

Atty. Docket No: 31869-198826

Customer No:

**26694**

PATENT TRADEMARK OFFICE

***Mail Stop: Amendment***

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

**AMENDMENT AND RESPONSE**

Sir:

This communication is responsive to the Office Action dated November 25, 2005, the time for responding thereto, based on the Next-Business-Day rule, is on or before February 27, 2006. See 37 C.F.R. § 1.7.

Please amend the above-identified application as follows:

**Amendments to the Specification** begin on page 2 of this paper.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 3 of this paper.

**Remarks** begin on page 8 of this paper.